



SBB4089Z

50MHz to 6000MHz Cascadable Amplifier

Product Overview

The SBB4089Z is a high performance InGaP HBT MMIC amplifier utilizing a Darlington configuration with an active bias network. The active bias network provides stable current over temperature and process Beta variations. Designed to run directly from a 5V supply, the SBB4089Z does not require a dropping resistor as compared to typical Darlington amplifiers. The SBB4089Z product is designed for high linearity 5V gain block applications that require small size and minimal external components. It is internally matched to 50Ω.

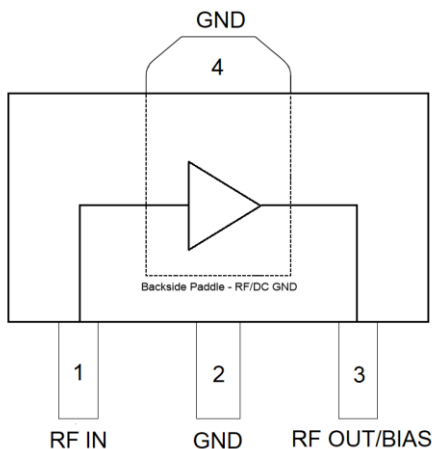


3-pin SOT-89 Package

Key Features

- 50MHz – 6000MHz Operational Frequency
- OIP3 = +35.2dBm at 1950MHz
- P1dB = +19.3dBm at 1950MHz
- Single Fixed 5V Supply
- Robust 1000V ESD, Class 1C
- Patented Thermal Design and Bias Circuit
- Low Thermal Resistance

Functional Block Diagram



Top View

Applications

- PA Driver Amplifier
- Cellular, PCS, GSM, UMTS
- Wideband Instrumentation
- Wireless Data, Satellite Terminals

Ordering Information

| Part No. | Description |
|--------------|--|
| SBB4089Z | 7" Reel with 1000 pieces |
| SBB4089ZPCK1 | 500MHz to 3500MHz PCBA with 5-piece sample bag |

Absolute Maximum Ratings

| Parameter | Rating |
|--|---------------|
| Storage Temperature | -55 to 150 °C |
| RF Input Power, CW, 50 Ω, T=+25 °C | +24 dBm |
| Device Voltage (V _{CC}) | +5.5 V |
| Device Current (I _{CC}) | 100 mA |
| Power Dissipation | 0.55 W |
| Junction Temperature (T _J) | +150 °C |

Exceeding any one or a combination of the Absolute Maximum Rating conditions may cause permanent damage to the device.

Recommended Operating Conditions

| Parameter | Min | Typ | Max | Units |
|-----------------------------------|-----|-----|-------|-------|
| Device Voltage (V _{CC}) | | +5 | +5.25 | V |
| T _{CASE} | -40 | | +105 | °C |

Electrical specifications are measured at specified test conditions. Specifications are not guaranteed over all recommended operating conditions. Application of conditions to the device outside the Recommended Operating Conditions may reduce device reliability and performance.

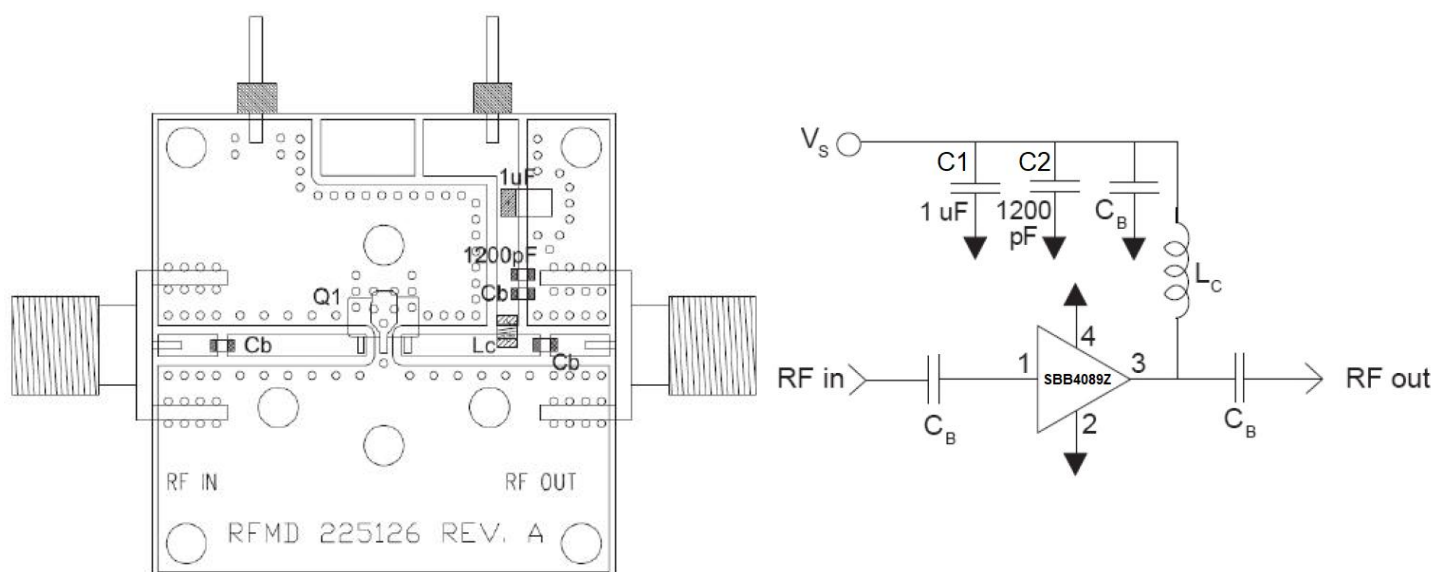
Electrical Specifications

| Parameter | Conditions ⁽¹⁾ | Min | Typ | Max | Units |
|-------------------------------------|--|------|-------|------|-------|
| Operational Frequency Range | | 50 | | 6000 | MHz |
| Gain | 850MHz | 14 | 15.5 | 17 | dB |
| | 1950MHz | 13.8 | 15 | 17 | dB |
| | 2000MHz | 13.5 | 15.5 | 17.5 | dB |
| Input Return Loss | 1950MHz | 10 | 16.3 | | dB |
| Output Return Loss | 1950MHz | 10 | 18 | | dB |
| Output P1dB | 850MHz | | +19 | | dBm |
| | 1950MHz | +18 | +19.2 | | dBm |
| Output IP3 | P _{out} = 0 dBm/tone, Δf = 1 MHz, Center Freq.= 850MHz | | +39 | | dBm |
| | P _{out} = 0 dBm/tone, Δf = 1 MHz, Center Freq.= 1950MHz | +33 | +35.3 | | dBm |
| Noise Figure | 1950MHz | | 4.3 | 5.5 | dB |
| Reverse Isolation | 1950MHz | | 18.5 | | dB |
| Device Operating Current | | 70 | 80 | 92 | mA |
| Thermal Resistance, θ _{Jc} | Junction to case | | 69.9 | | °C/W |

Notes:

1. Test conditions unless otherwise noted: V_{CC} = +5.0 V, I_{CC} = 80mA, Temp= +25 °C, 50 Ω system.

Evaluation Board



Bill of Material

| Reference Des. | Value | Description | Manuf. | Part Number |
|----------------|---------|-----------------------------|-----------|----------------|
| n/a | n/a | Printed Circuit Board | Qorvo | |
| Q1 | n/a | Cascadable Amplifier | Qorvo | SBB4089Z |
| Cb | 68 pF | CAP, 0603, 5%, 50V, C0G | various | |
| C1 | 1 uF | CAP, 1206, 20%, 20V, TANT-A | various | |
| C2 | 1200 pF | CAP, 0603, 10%, 50V, X7R | various | |
| Lc | 82 nH | IND, 1008, 5%, W/W | Coilcraft | 1008CS-820XJRC |

Typical Performance

Test conditions unless otherwise noted: $V_{CC} = +5.0V$, $I_{CC} = 80mA$, Temp = $+25^{\circ}C$, 50Ω system.

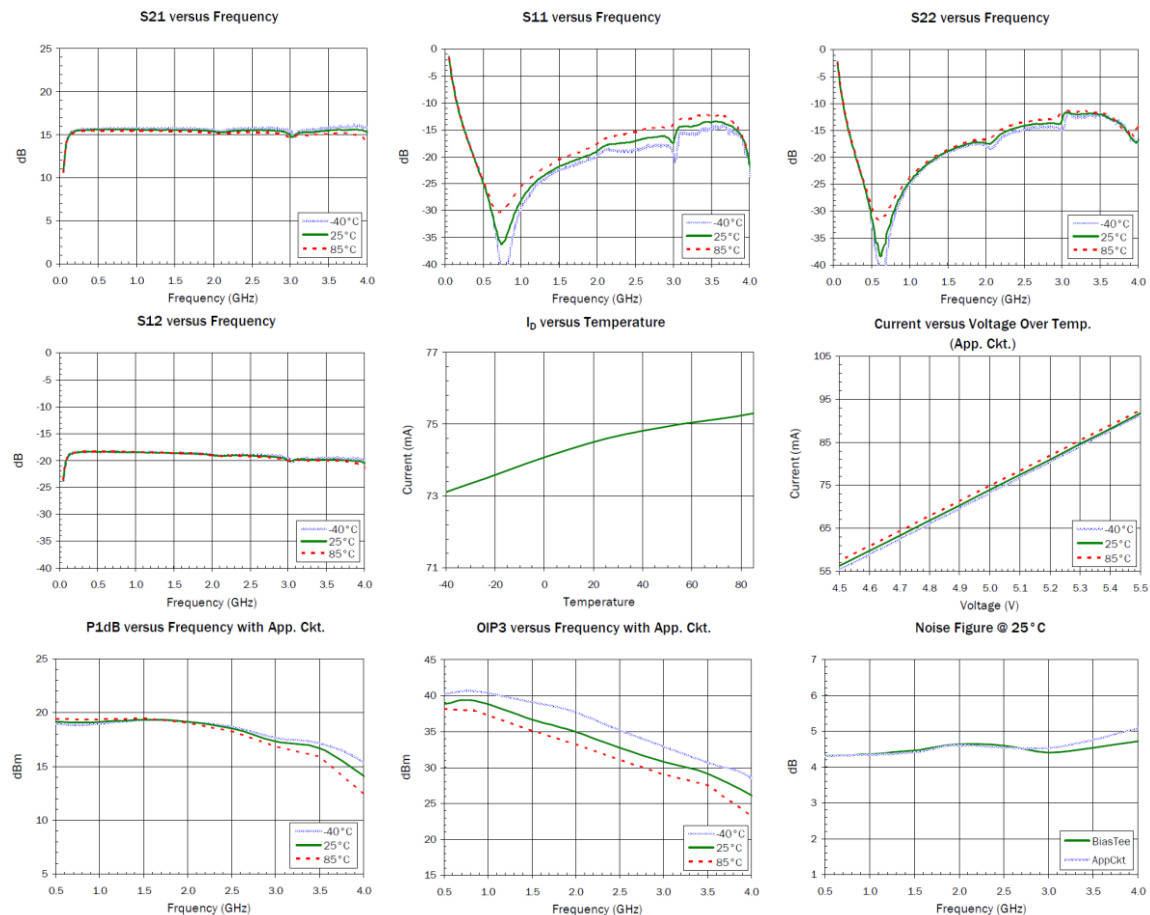
| Parameter | Typical Value | | | | | | | | | Units |
|---------------------|---------------|-------|-------|-------|-------|-------|-------|-------|-------|-------|
| Frequency | 50 | 100 | 200 | 500 | 850 | 1950 | 2500 | 3500 | 4000 | MHz |
| Gain | 16.3 | 15.7 | 15.7 | 15.6 | 15.6 | 15.5 | 15.5 | 15.5 | 15 | dB |
| Input Return Loss | 11.2 | 16.3 | 22.4 | 25.1 | 29.9 | 19.4 | 17.6 | 14.9 | 21.3 | dB |
| Output Return Loss | 19.4 | 25.4 | 29.8 | 32.1 | 26.4 | 17.2 | 14.7 | 13.2 | 17.4 | dB |
| Reverse Isolation | 17.7 | 17.8 | 17.8 | 18.4 | 18.4 | 18.9 | 19.1 | 19.8 | 20.8 | dB |
| Output P1dB | +18.5 | +18.7 | +19 | +19.2 | +19.1 | +19.2 | +18.6 | +16.7 | +14.1 | dBm |
| OIP3 ⁽¹⁾ | +38.7 | +40.6 | +39.7 | +38.8 | +39.3 | +35.2 | +32.8 | +29.1 | +26.1 | dBm |
| Noise Figure | 4.3 | 4.3 | 4.3 | 4.3 | 4.3 | 4.6 | 4.5 | 4.8 | 5.1 | dB |

Notes:

1. $P_{out} = 0$ dBm/tone, $\Delta f = 1$ MHz

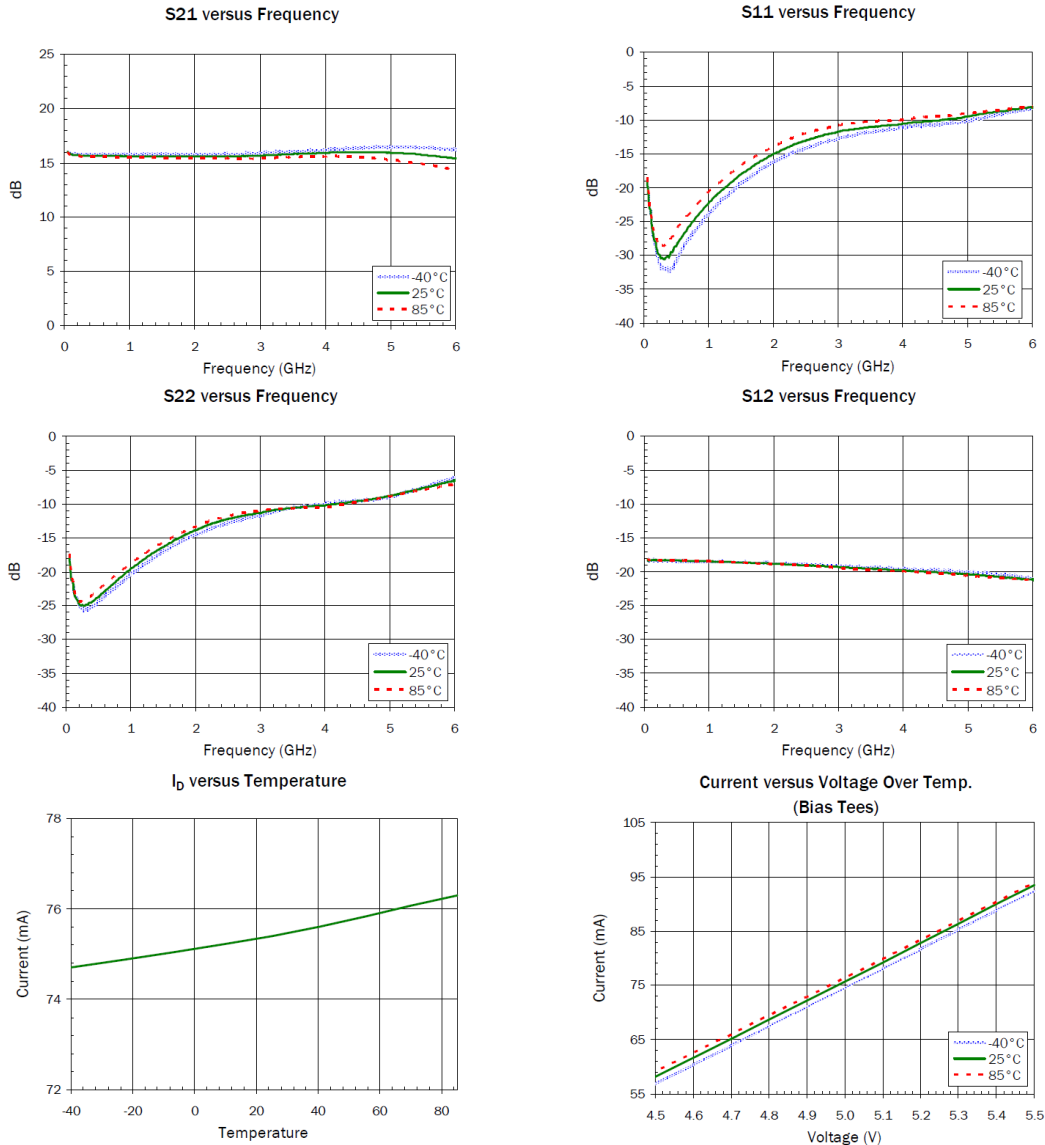
Performance Plots

Test conditions unless otherwise noted: $V_{CC} = +5.0V$, $I_{CC} = 80mA$, Temp = $+25^{\circ}C$, 50Ω system.

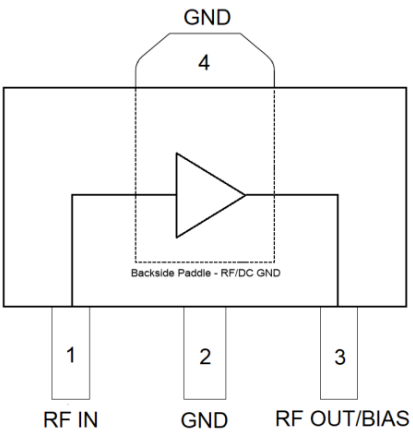


Performance Plots – with BIAS Tees

Test conditions unless otherwise noted: $V_{CC} = +5.0V$, $I_{CC} = 80mA$, Temp = $+25^{\circ}C$, 50Ω system.



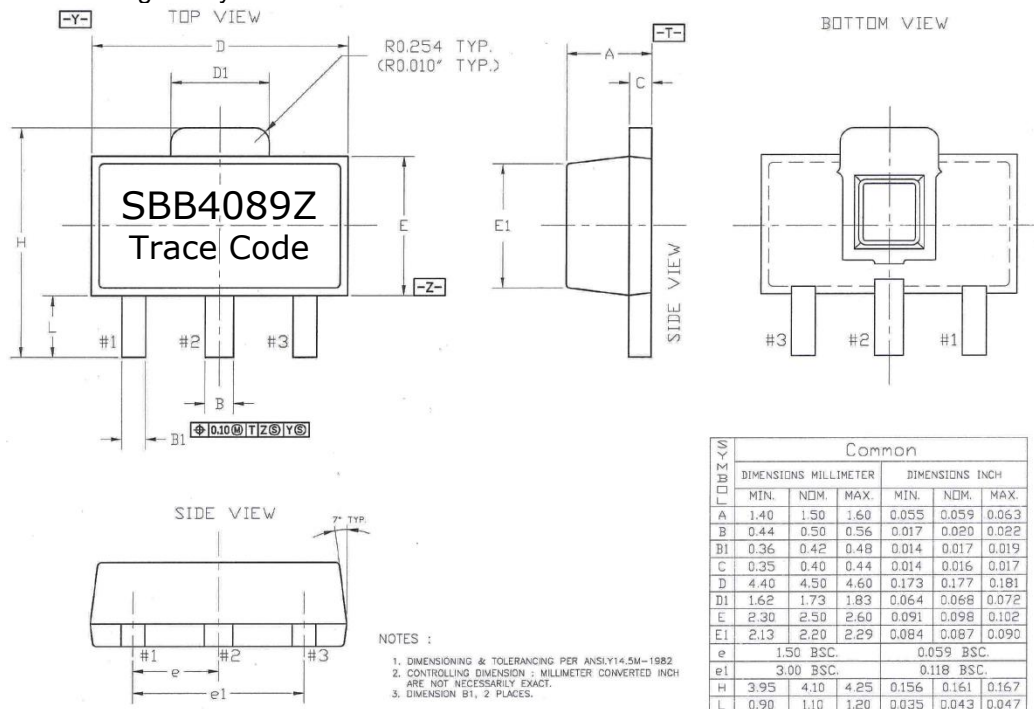
Pin Configuration and Description



| Pin No. | Label | Description |
|---------|---------------|---|
| 1 | RF IN | RF input. External DC Block capacitor required. |
| 2, 4 | GND | RF/DC ground. Use recommended via hole pattern to minimize inductance and thermal resistance. See PCB Mounting Pattern for suggested footprint. |
| 3 | RF OUT / BIAS | RF output and DC Supply input. External DC Block capacitor and bias voltage required. |

Package Marking and Dimensions

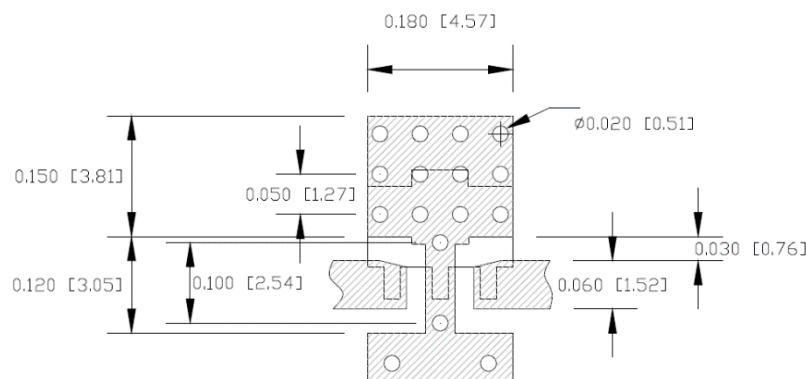
Marking: Part Identifier – SBB4089Z
Trace Code – Assigned by sub-contractor



Notes:

1. All dimensions are in millimeters. Angles are in degrees.
2. The terminal #1 identifier and terminal numbering conform to JESD 95-1 SPP-012.

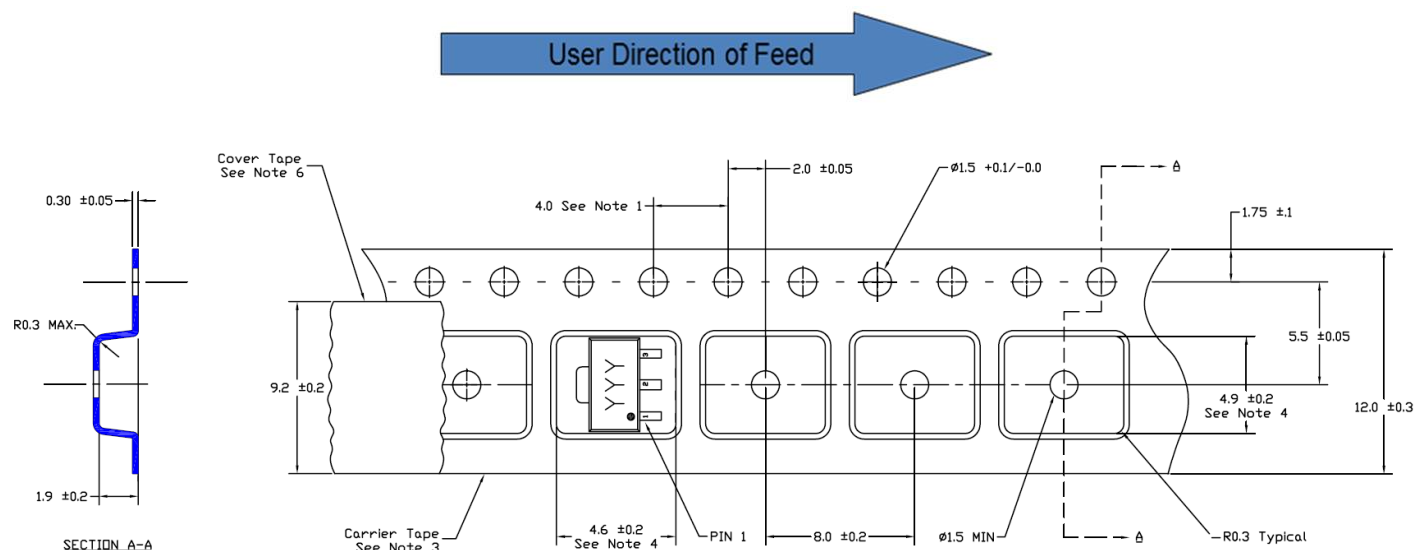
PCB Mounting Pattern



NOTES:

1. All dimensions are in millimeters [inches]. Angles are in degrees.
2. Use 1 oz. copper minimum for top and bottom layer metal.
3. Vias are required under the backside paddle of this device for proper RF/DC grounding and thermal dissipation. We recommend a 0.35mm (#80/.0135") diameter bit for drilling via holes and a final plated thru diameter of 0.25mm (0.01").
4. Ensure good package backside paddle solder attach for reliable operation and best electrical performance.

Tape and Reel Information – Carrier and Cover Tape Dimensions

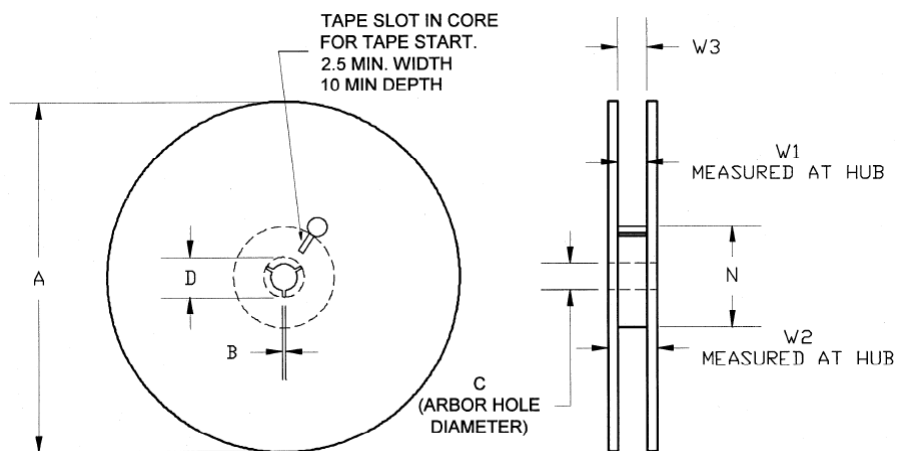


1. 10 sprocket hole pitch cumulative tolerance
2. Camber not to exceed 1mm in 100mm
3. Material: Black Conductive Polystyrene
4. Measured on a plane 0.3mm above the bottom of the pocket.
5. Measured from a plane on the inside bottom of the pocket to the top surface of the carrier.
6. Material: Antistatic Polyester Film

| Feature | Measure | Symbol | Size (in) | Size (mm) |
|---------------------|--|--------|-----------|-----------|
| Cavity | Length | A0 | 0.181 | 4.60 |
| | Width | B0 | 0.193 | 4.90 |
| | Depth | K0 | 0.075 | 1.90 |
| | Pitch | P1 | 0.315 | 8.00 |
| Centerline Distance | Cavity to Perforation - Length Direction | P2 | 0.079 | 2.00 |
| | Cavity to Perforation - Width Direction | F | 0.217 | 5.50 |
| Cover Tape | Width | C | 0.362 | 9.20 |
| Carrier Tape | Width | W | 0.472 | 12.0 |

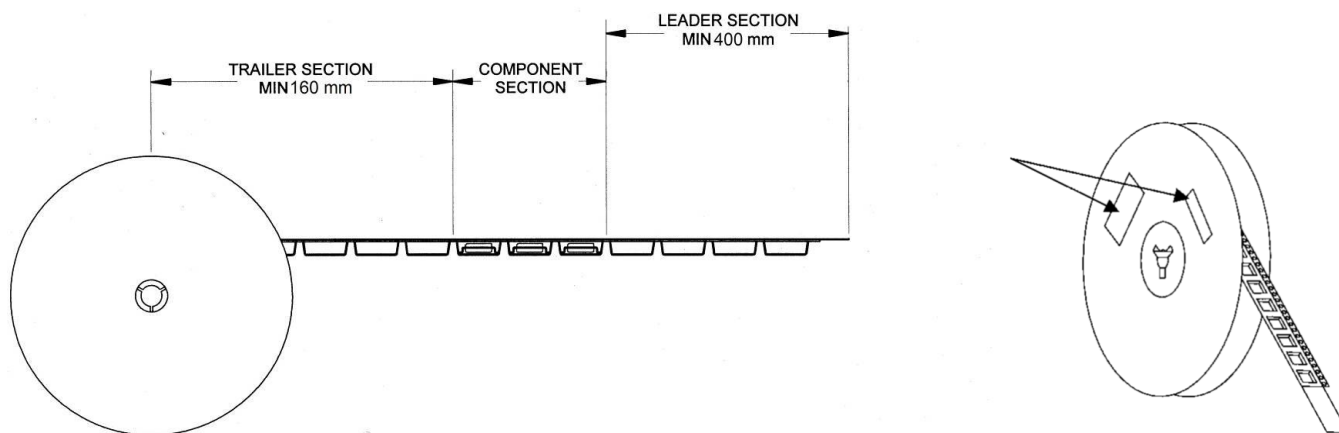
Tape and Reel Information – Reel Dimensions

Standard T/R size = 1,000 pieces on a 7" reel.



| Feature | Measure | Symbol | Size (in) | Size (mm) |
|---------|----------------------|--------|-----------|-----------|
| Flange | Diameter | A | 6.969 | 177.0 |
| | Thickness | W2 | 0.717 | 18.2 |
| | Space Between Flange | W1 | 0.504 | 12.8 |
| Hub | Outer Diameter | N | 2.283 | 58.0 |
| | Arbor Hole Diameter | C | 0.512 | 13.0 |
| | Key Slit Width | B | 0.079 | 2.0 |
| | Key Slit Diameter | D | 0.787 | 20.0 |

Tape and Reel Information – Tape Length and Label Placement



Notes:

1. Empty part cavities at the trailing and leading ends are sealed with cover tape. See EIA 481-1-A.
2. Labels are placed on the flange opposite the sprockets in the carrier tape.

Handling Precautions

| Parameter | Rating | Standard |
|----------------------------------|----------|---------------------------|
| ESD – Human Body Model (HBM) | Class 1C | ESDA / JEDEC JESDC22-A114 |
| MSL – Moisture Sensitivity Level | Level 3 | IPC/JEDEC J-STD-020 |



Caution!
ESD-Sensitive Device

Solderability

Compatible with both lead-free (260°C max. reflow temp.) and tin/lead (245°C max. reflow temp.) soldering processes. Solder profiles available upon request.

Contact plating: NiPdAu

RoHS Compliance

This part is compliant with 2011/65/EU RoHS directive (Restrictions on the Use of Certain Hazardous Substances in Electrical and Electronic Equipment) as amended by Directive 2015/863/EU.

This product also has the following attributes:

- Lead Free
- Halogen Free (Chlorine, Bromine)
- Antimony Free
- TBBP-A (C₁₅H₁₂Br₄O₂) Free
- PFOS Free
- SVHC Free



Contact Information

For the latest specifications, additional product information, worldwide sales and distribution locations:

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