



BSS84W

P-CHANNEL ENHANCEMENT MODE FIELD EFFECT TRANSISTOR

Product Summary

BV _{DSS}	Rds(on)	I _D T _A = +25°C
-50V	10Ω @ V _{GS} = -5V	-130mA

Description and Applications

This MOSFET has been designed to minimize the on-state resistance (RDS(ON)) yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- General Purpose Interfacing Switch
- Power Management Functions
- Analog Switch

Features and Benefits

- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative.

 https://www.diodes.com/quality/product-definitions/

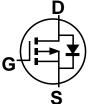
Mechanical Data

- Case: SOT323
- Case Material: Molded Plastic, "Green" Molding Compound, UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Solderable per MIL-STD-202, Method 208
 Lead Free Plating (Matte Tin Finish Annealed over Alloy 42
 Leadframe).
- Weight: 0.006 grams (Approximate)

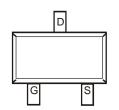
SOT323



Top View



Equivalent Circuit



Top View

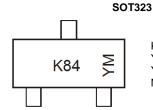
Ordering Information (Note 4)

Part Number	Case	Packaging
BSS84W-7-F	SOT323	3,000 / Tape & Reel

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



K84 = Product Type Marking Code YM or \overline{Y} M = Date Code Marking Y or \overline{Y} = Year (ex: H = 2020) M = Month (ex: 9 = September)

Date Code Key

Year	2007		2020	2021	2022	2023	2024	2025	2026	2027	2028	2029
Code	U		Η	- 1	J	K	L	М	N	0	Р	R
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	VDSS	-50	V	
Drain-Gate Voltage (Note 5)		Vdgr	-50	V
Gate-Source Voltage	Continuous	Vgss	±20	V
Drain Current (Note 5)	Continuous	ID	-130	mA
Pulsed Drain Current (Note 5)		IDM	-1	А

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	PD	200	mW
Thermal Resistance, Junction to Ambient	Reja	625	°C/W
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	°C

Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 6)							
Drain-Source Breakdown Voltage	BV _{DSS}	-50	-75	_	V	Vgs = 0V, ID = -250µA	
Zero Gate Voltage Drain Current	IDSS	_ _ _	-	-1 -2 -100	μΑ μΑ nA	V _{DS} = -50V, V _{GS} = 0V, T _J = +25°C V _{DS} = -50V, V _{GS} = 0V, T _J = +125°C V _{DS} = -25V, V _{GS} = 0V, T _J = +25°C	
Gate-Body Leakage	Igss	_	_	±10	nA	$V_{GS} = \pm 20V$, $V_{DS} = 0V$	
ON CHARACTERISTICS (Note 6)							
Gate Threshold Voltage	V _{GS(TH)}	-0.8	-1.6	-2.0	V	$V_{DS} = V_{GS}$, $I_D = -1mA$	
Static Drain-Source On-Resistance	R _{DS} (ON)	_	6	10	Ω	V _G S = -5V, I _D = -0.1A	
Forward Transconductance	grs	0.05	_	_	S	V _{DS} = -25V, I _D = -0.1A	
DYNAMIC CHARACTERISTICS (Note 7)							
Input Capacitance	Ciss	_	_	45	pF		
Output Capacitance	Coss	_	_	25	pF	$V_{DS} = -25V, V_{GS} = 0V, f = 1.0MHz$	
Reverse Transfer Capacitance	Crss	_	_	12	pF		
SWITCHING CHARACTERISTICS (Note 7)							
Turn-On Delay Time	tD(ON)	_	10	_	ns	V _{DD} = -30V, I _D = -0.27A,	
Turn-Off Delay Time	t _{D(OFF)}	_	18	_	ns	$R_{GEN} = 50\Omega$, $V_{GS} = -10V$	

Notes:

- 5. Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch; pad layout as shown on Diodes Incorporated's suggested pad layout document, which can be found on our website at http://www.diodes.com/package-outlines.html.

 6. Short duration pulse test used to minimize self-heating effect.
- 7. Guarantee by design. Not subject to production testing.



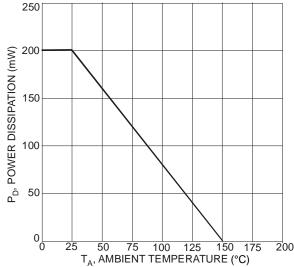
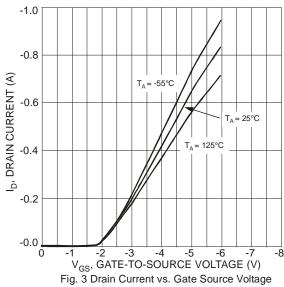
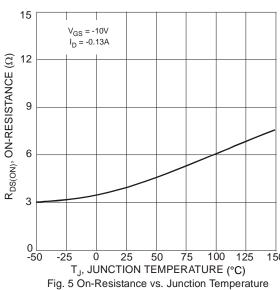
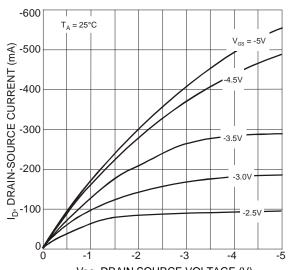


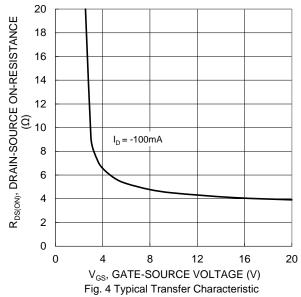
Fig. 1 Max Power Dissipation vs. Ambient Temperature







V_{DS}, DRAIN SOURCE VOLTAGE (V) Fig. 2 Drain Source Current vs.Drain Source Voltage



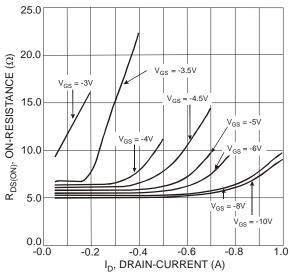


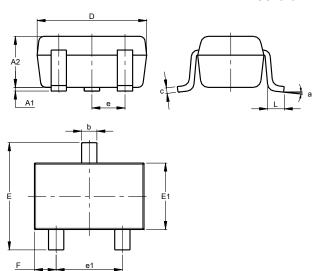
Fig. 6 On-Resistance vs. Drain-Current



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

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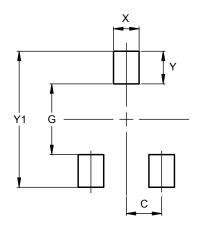


SOT323						
Dim	Min	Max	Тур			
A1	0.00	0.10	0.05			
A2	0.90	1.00	0.95			
b	0.25	0.40	0.30			
С	0.10	0.18	0.11			
D	1.80	2.20	2.15			
Е	2.00	2.20	2.10			
E1	1.15	1.35	1.30			
е	0.650 BSC					
e1	1.20	1.40	1.30			
F	0.375	0.475	0.425			
L	0.25	0.40	0.30			
а	0°	8°				
All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT323



Dimensions	Value (in mm)
С	0.650
G	1.300
Х	0.470
Y	0.600
Y1	2.500



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