



Product Summary

BV _{DSS}	Rds(on) max	I _D Tc = +25°С
700V	$1.4\Omega @ V_{GS} = 10V$	6.1A

N-CHANNEL ENHANCEMENT MODE MOSFET

Features and Benefits

- Low On-Resistance
- High BV_{DSS} Rating for Power Application
- Low Input Capacitance
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please <u>contact us</u> or your local Diodes representative. <u>https://www.diodes.com/quality/product-definitions/</u>

Description and Applications

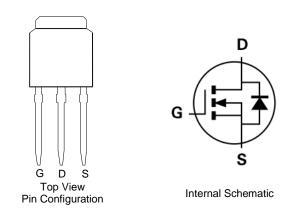
This MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) and yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

- Adaptor
- LCD & PDP TV
- Lighting

TO251 (Type TH)

Mechanical Data

- Case: TO251
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Terminal Connections: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 3
- Weight: 0.33 grams (Approximate)



Ordering Information (Note 4)

Part Number	Case	Packaging
DMJ70H1D4SJ3	TO251 (Type TH)	75 pieces / Tube

Notes: 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.

2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



 $\begin{array}{l} O \mid I = \text{Manufacturer's Marking} \\ \text{70H1D4} = \text{Product Type Marking Code} \\ \text{YY}\underline{WW} = \text{Date Code Marking} \\ \text{YY} = \text{Last Two Digits of Year (ex: 19 = 2019)} \\ \underline{WW} = \text{Week Code (01 to 53)} \end{array}$



Maximum Ratings (@TA = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Drain-Source Voltage		V _{DSS}	700	V
Gate-Source Voltage		Vgss	±30	V
Continuous Drain Current (Notes 5, 9) V _{GS} = 10V	T _C = +25°C T _C = +100°C	ID	6.1 3.9	A
Maximum Body Diode Forward Current (Note 6)		ls	6.1	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)		ldм	24	А
Pulsed Source Current (10µs Pulse, Duty Cycle = 1%)		lsм	24	А
Avalanche Current	L = 60mH	las	0.6	А
Avalanche Energy	L = 60mH	E _{AS}	10	mJ
Peak Diode Recovery dv/dt (Note 7)		dv/dt	7.6	V/ns

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	$T_{C} = +25^{\circ}C$	D-	78	W
Total Power Dissipation (Note 5)	T _C = +100°C	PD	31	
Thermal Resistance, Junction to Ambient (Note 6)		Reja	72	°C/W
Thermal Resistance, Junction to Case (Note 5)		Rejc	1.6	C/VV
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)							
Drain-Source Breakdown Voltage	BV _{DSS}	700	_	—	V	$V_{GS} = 0V, I_D = 250 \mu A$	
Zero Gate Voltage Drain Current	IDSS	—	_	1	μA	V _{DS} = 700V, V _{GS} = 0V	
Gate-Source Leakage	Igss	—	_	100	nA	$V_{GS} = \pm 30V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)			-				
Gate Threshold Voltage	VGS(TH)	2	3.8	5	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$	
Static Drain-Source On-Resistance	RDS(ON)	_	1.26	1.4	Ω	VGS = 10V, ID = 1A	
Diode Forward Voltage	Vsd	_	0.88	1.3	V	VGS = 0V, IS = 5A	
DYNAMIC CHARACTERISTICS (Note 7)			•	•	•	·	
Input Capacitance	Ciss	—	273	—		V _{DS} = 100V, f = 1MHz, V _{GS} = 0V	
Output Capacitance	Coss	_	16	—	pF		
Reverse Transfer Capacitance	Crss	_	1.5	—			
Gate Resistance	Rg	_	3.9	—	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$	
Total Gate Charge	Qg	_	9	—		V _{DD} = 480V, I _D = 1.5A, V _{GS} = 10V	
Gate-Source Charge	Qgs	_	1.9	—	nC		
Gate-Drain Charge	Qgd	_	3.6	—		VGS = 10V	
Turn-On Delay Time	t _{D(ON)}	_	7	—		V _{DD} = 400V, V _{GS} = 13V,	
Turn-On Rise Time	t _R	_	7	—	ns		
Turn-Off Delay Time	tD(OFF)	_	27	—		$R_g = 10.2\Omega, I_D = 1.5A$	
Turn-Off Fall Time	t _F		15	—]		
Body Diode Reverse Recovery Time	t _{RR}	_	134	—	ns		
Body Diode Reverse Recovery Charge	QRR	—	0.7	—	μC	Is = 1.5A, dl/dt = 100A/µs	

Notes: 5. Device mounted on an infinite heatsink.

6. Device mounted on FR-4 substrate PC board, 2oz. copper, with minimum recommended pad layout.

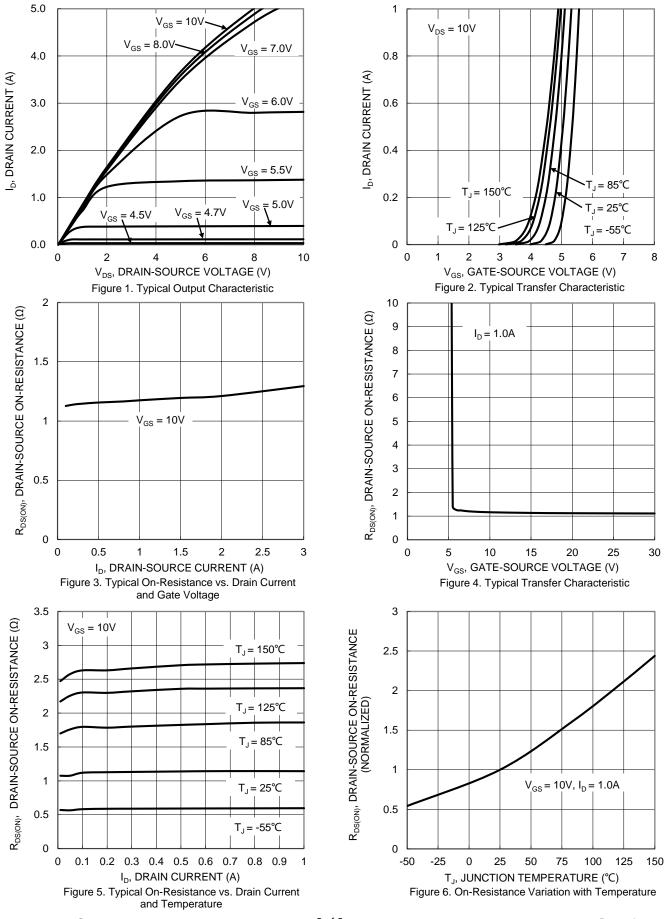
7. Guaranteed by design. Not subject to production testing.

8. Short duration pulse test used to minimize self-heating effect.

9. Drain current limited by maximum junction temperature.



DMJ70H1D4SJ3



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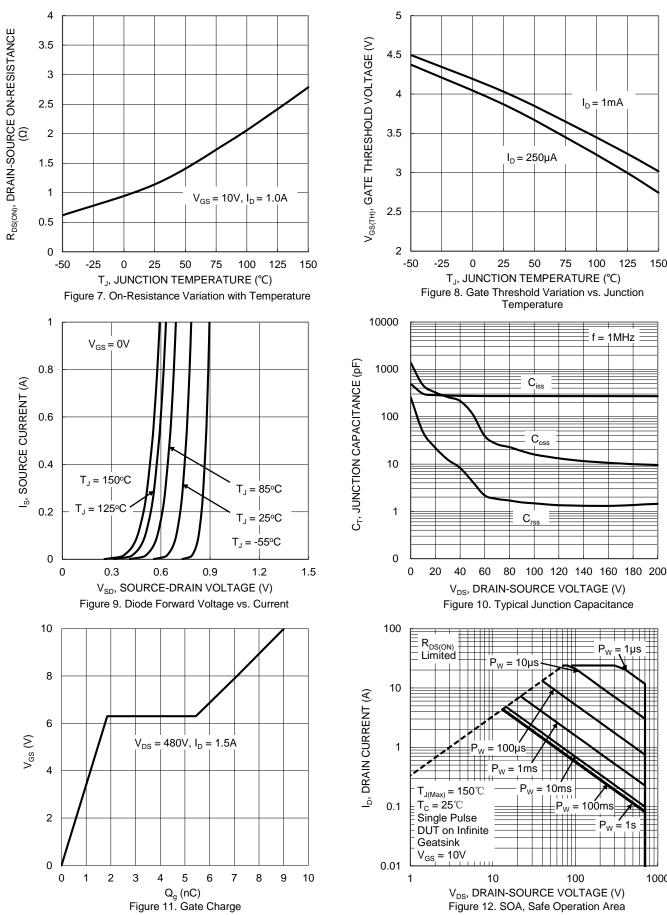
DMJ70H1D4SJ3

100

125

f = 1MHz

150



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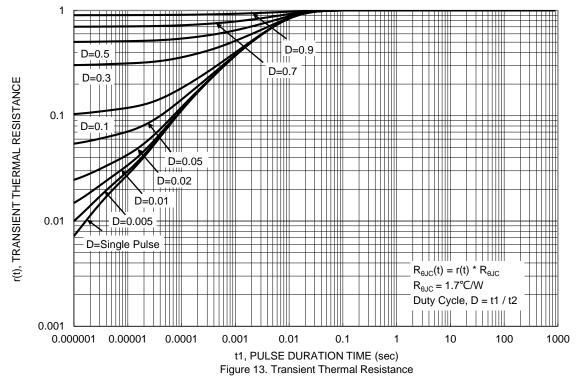
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1000

 P_{W} 1s

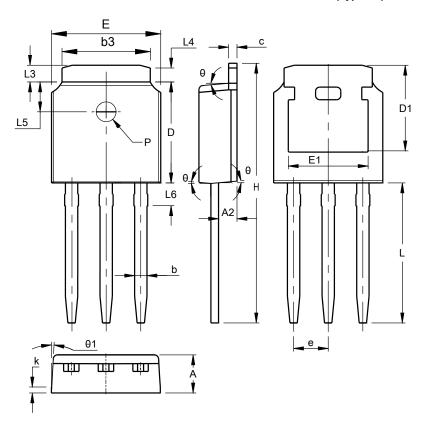
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Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.



TO251 (Type TH)

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Dim	Min	Max	Тур		
Α	2.20	2.40	2.30		
A2	0.97	1.17	1.07		
b	0.68	0.90	0.78		
b3	5.20	5.50	5.33		
С	0.43	0.63	0.53		
D	5.98	6.22	6.10		
D1	5	.30 RE	F		
е	2.	286 BS	C		
ш	6.40	6.80	6.60		
E1	4.63	5.03	4.83		
H	16.22	16.82	16.52		
k	0.40REF				
L	9.15	9.65	9.40		
L3	0.88	1.28	1.02		
L4	0.75 REF				
L5	1.65	1.95	1.80		
L6	0.85	1.25	1.05		
PØ	1.20				
θ	5°	9°	7°		
θ1	5°	9°	7°		
All D	All Dimensions in mm				



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